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Winbond HyperRAM™ & SpiStack® and Renesas RZ/A2M accelerate the construction of embedded artificial intelligence (AI) systems

Furthermore, in image processing related to quality control applications ... and Gajski D., 1994. Specification and Design of Embedded Systems. Printice Hall Editor, Englewood Cliffs, NJ. [12] Fisher ...

A Real-Time Image Processing with a Compact FPGA-Based Architecture

Presented as a virtual event in May, the Embedded Vision Summit examined the latest developments in practical computer vision and AI edge processing. In my role as the summit's general chair, I ...

5 Trends to Watch in Embedded Vision and Edge AI

Large numbers of sensors, massive amounts of data, ever-increasing computing power, real-time operation and security concerns required for autonomous vehicles are driving the core of computation from ...

Autonomous vehicles: AI must accelerate

The expansion also allows VVDN to address multiple-tier including high as well as mid-range requirements for smart camera solutions VVDN has gained expertis ...

VVDN expands its capabilities on Ambarella edge AI vision

TCL isn't new to the smartphone game by any stretch of the imagination but its TCL 20S -- recently sent to us for review -- is only in the second ...

TCL 20S Review - A Great, Good-Looking Phone Mostly Held Back By Its Price

Surging demand for semiconductors from several industries, along with government and private investments in the semiconductor industry to address the global chip shortage, should help popular chip ...

Advanced Micro Devices vs. Texas Instruments: Which Semiconductor Stock is a Better Buy?

VVDN Technologies, a premier electronic product engineering and manufacturing company, announces the expansion of its capabilities ...

VVDN expands its capabilities on Ambarella edge AI vision SoC platform to deliver next-gen vision based solutions

Xylon offers a complete logicBRICKS IP suite for implementing High-Dynamic Range (HDR) Image Signal Processing (ISP) pipelines in embedded designs based on Xilinx FPGA, SoC, MPSoC or ACAP programmable ...

HDR ISP framework for multi-camera applications

Continental Access, a division of NAPCO Security Technologies, Inc., is pleased to introduce Michael Venoit as the new Continental Access Vice President of Sales.Michael Venoit comes to ...

NAPCO Security Technologies, Inc. appoints Michael Venoit as the new Vice President of Sales of Continental Access

Knowing how and when to use GPGPU in HPEC computing will give embedded designers ... that are using the advanced processing capabilities for radars, image recognition, classification, motion ...

11 Myths About GPGPU Computing

IMCO Group announced the acquisition of EMT Electronics Manufacturing Technologies Ltd. subject to suspending conditions.EMT specialises in the design, manufacturing, and testing of ...

IMCO Industries Ltd. acquires Electronics Manufacturing Technology

Industry leaders including Microsoft, Google and Facebook have all integrated Simultaneous Localization and Mapping (SLAM) technology into their AR/VR headsets using low power, embedded processors ...

SLAM without a PhD

Image processing specialist Ambarella has launched two new SoCs ... That's only possible if you design the whole architecture from scratch, knowing exactly what the chip is going to do." The AI ...

AI-enabled SoCs handle multiple video streams

In addition, the Blaize P1600 can convert the depth camera's USB output to high-speed Ethernet connectivity, for enhanced video processing. Software development kits for the reference design ...

Blaize and eYs3D Microelectronics Unveil New Reference Design for Enhanced 3D Computer Vision for Edge AI

All participants will be able to discuss use cases and requirements for new interoperability standards to accelerate market growth and reduce development costs in embedded markets ... sophisticated ...

Machine vision groups create group for exploring API standards

What are the main automotive design challenges addressed by each ... (including GTM4 timers), fast analog capability with embedded signal processing, dedicated sensor/actuator interfaces, and ...